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Homayoun, Aliakbar and Razavi, Behzad 2015. A Low-Power CMOS Receiver for 5 GHz WLAN. IEEE Journal of Solid-State Circuits, Vol. 50, Issue. 3, p. 630.

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A Highly Linear and Efficient 28-GHz PA With a P sat of 23.2 dBm, P 1 dB of 22.7 dBm, and PAE of 35.5% in 65-nm Bulk CMOS. IEEE Journal of Solid-State Circuits, Vol. 56, Issue. 3, p. 914.

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