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Arm Primecell Technical Reference Manual

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Documentation - Arm Developer

ARM PrimeCell Real Time Clock (PL031) Technical Reference Manual(TRM). This document describes the RTC functions, programming registers, and signals. The RTC has an AMBA APB interface, which connects to a host processor.

ARM PrimeCell Real Time Clock (PL031) Technical Reference ...

ARM's developer website includes documentation, tutorials, support resources and more. ...
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This preface introduces the PrimeCell Generic Interrupt Controller (PL390) Technical Reference Manual(TRM). It contains the following sections: •About this book on page x •Feedback on page xiii.

PrimeCell Generic Interrupt Controller (PL390) Technical ...

This document is a technical reference manual for the ARM PrimeCell GPIO (PL061). Intended audience This document has been written for hardware and software engineers implementing System-on-Chip designs. It provides information to enable designers to integrate the peripheral into a target system as quickly as possible. Using this manual

ARM PrimeCell General Purpose Input/Output (PL061 ...

Home Documentation ddi0190 b - ARM PrimeCell General Purpose Input/Output (PL061) Technical Reference Manual Functional Overview PrimeCell GPIO functional description ARM PrimeCell General Purpose Input/Output (PL061) Technical Reference Manual

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ARM PrimeCell Multimedia Card Interface (PL180) Technical ...

This manual is written for implementation engineers and architects, and provides a description of an optimal PrimeCell SMC architecture. The PrimeCell SMC provides an interface between the Advanced High-performance Bus(AHB) system bus and external (off-chip) memory devices.

PrimeCell Static Memory Controller (PL092) Technical ...

Page 10: About This Manual Preface About this manual This is the Technical Reference Manual (TRM) for the PrimeCell AHB SRAM/NOR Memory Controller.

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PrimeCell Vectored Interrupt Controller (PL192) Technical Reference Manual (ARM DDI 0273)
PrimeCell Core Identification Module (PL321) r0p0 Technical Reference Manual (ARM DDI 0327).

PrimeCell Inter-Processor Communications ... - Arm Developer

This document is the technical reference manual for the ARM PrimeCell Vectored Interrupt Controller(VIC).

ARM PrimeCell Vectored Interrupt Controller (PL192 ...

This preface introduces the PrimeCell AXI Configurable Interconnect (PL300) Technical Reference Manual. It contains the following sections: †About this manual on page x †Feedback on page xv.

PrimeCell AXI Configurable Interconnect (PL300) Technical ...

About this manual This is the technical reference manual for the ARM PrimeCell UART (PL011).

PrimeCell UART (PL011) - Taylor Johnson

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ARM DDI 0181E About this manual This is the Technical Reference Manual for the ARM PrimeCell Vectored Interrupt Controller (VIC). Product revision status The r npn identifier indicates the revision status of the product described in this manual, where: rn Identifies the major revision of the product.

PrimeCell Vectored Interrupt Controller (PL190) Technical ...

This document is a Technical Reference Manual (TRM) for the PrimeCell MultiPort Memory Controller (MPMC) (PL175). Product revision status The rnpn identifier indicates the revision status of the product described in this document, where: rn Identifies the major revision of the product. pn Identifies the minor revision or modification status of the product.

PrimeCell MultiPort Memory Controller (PL175) Technical ...

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